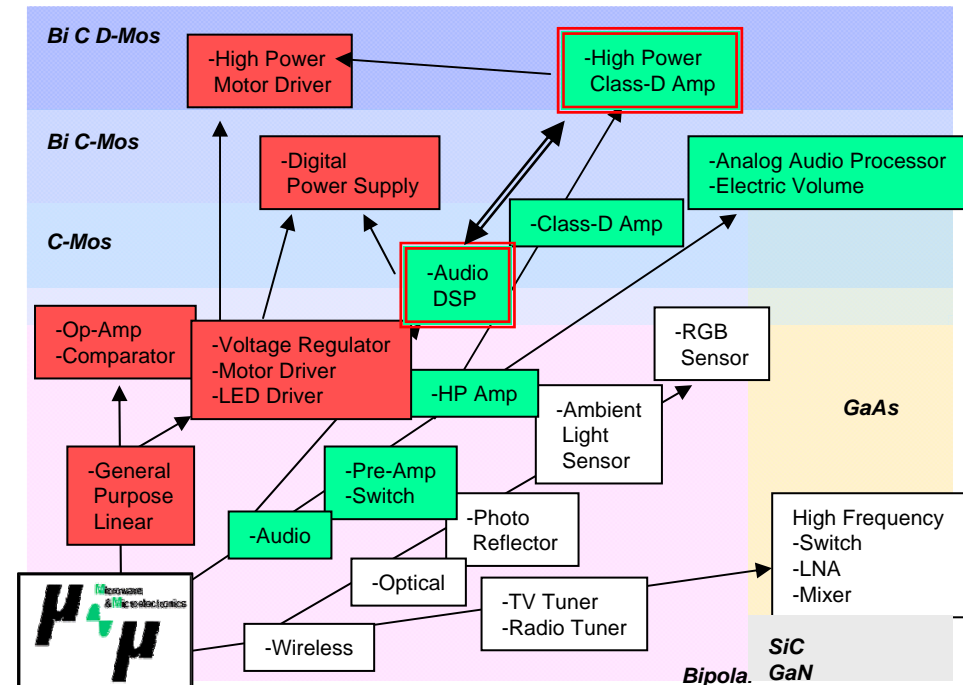
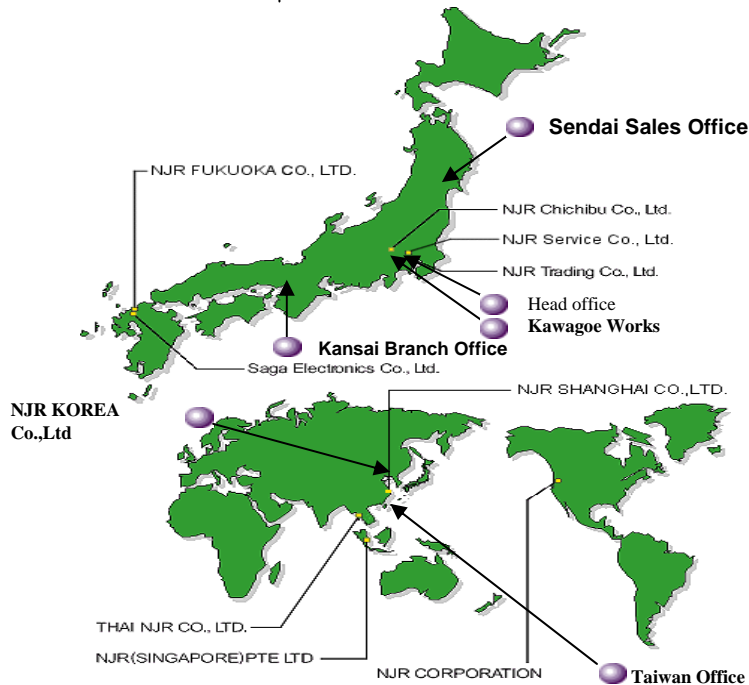
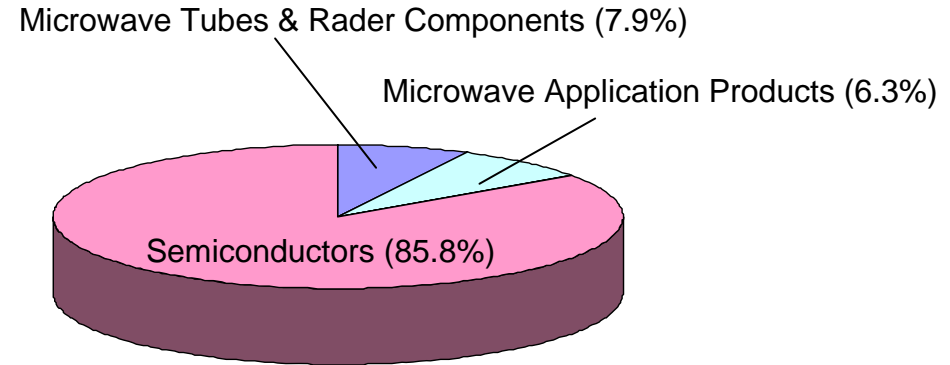




New Japan Radio Co.,Ltd

• Company Name	New Japan Radio Co.,Ltd.	
• Established	September 8 ,1959	
• Headquarter	3-10, Nihonbashi Yokoyama-cho, Chuo-ku, Tokyo 103-8456, Japan	
• Capital	5,220,075,750 yen (as of MAR 31,2010)	
• Number of stocks issued	39,131,000 (as of March 31,2010)	
• Stock Exchange Listing	September 2,2002 1st Section,Tokyo Stock Exchange	
• Net Sales	40,287 millions of yen *1(as of March 31,2010) 33,439 millions of yen *2(as of March 31,2010)	
• Number of Employees	3,163 *1 (as of March 31,2010)	*1;Consolidated
	1,603 *2 (as of March 31,2010)	*2;Non-Consolidated



Core Competence

New Japan Radio Co.,Ltd.

Future of NJU20010/20011(Alligator)

NJU20010(LQFP52)/NJU20011(LQFP64)



CLK watcher	Control / Reset / Interrupt /COP	JTAG
PWR watcher		Debug IF
Ext. interrupt	Data Memory RAM(2kW)	DSP core (XIMO16A)
CLK generator		
Real Timer		
I2C		
UART		
SPI		
Program memory		General Purpose Timer (GPT)
RAM(4kW)	Flash-ROM (16kW)	PWM generation (PSPWM)
A/D converter (PSATD)		Comparator (COMP3)

- DSP core : 62.5MHz (6-stage pipeline)
- SAR type A/D converter (12ch./12-bit/2Msps/8SH)
(Successive Approximation Register)
- PWM output : 1GHz resolution (Max.)

- Built-in 3 Comparators (20ns response)
- Program Flash-ROM: 16kW + RAM(4kW)
- Data memory : Dual-Port RAM 2kW
- 64-pin / 52-pin LQFP,
- Operation Voltage : 3.3V / 1.8V
- System control : Clock / Reset / Interrupt
- System protection : LVI(Low Volt Inhibition) /
Clock detector / Watch dog timer-detector
- JTAG-IF
- I2C / UART / SPI
- General purpose timer
- PWM (8 outputs) Capture(IC/OC)