

Technology Enabling for Specialty Foundry

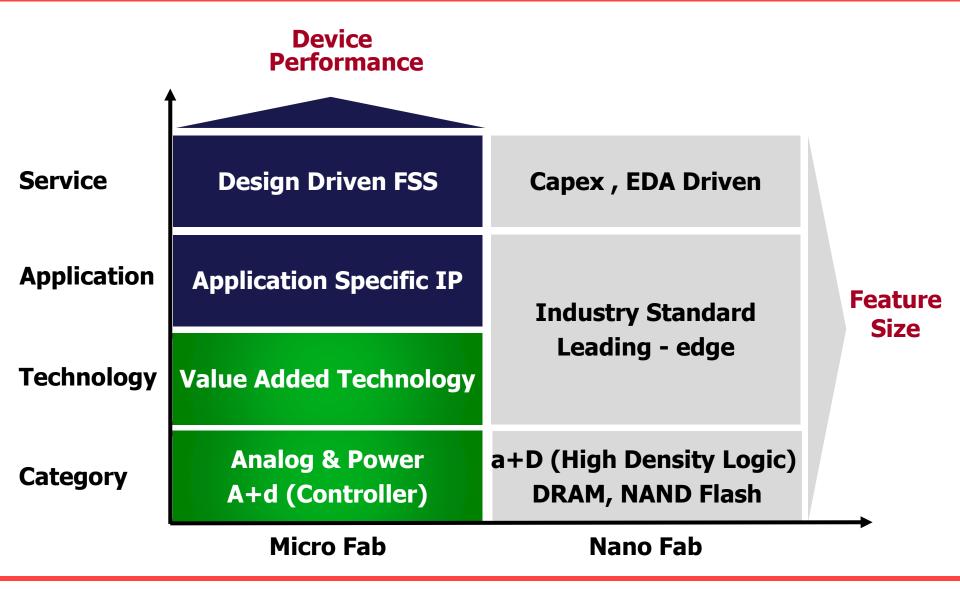
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Outline

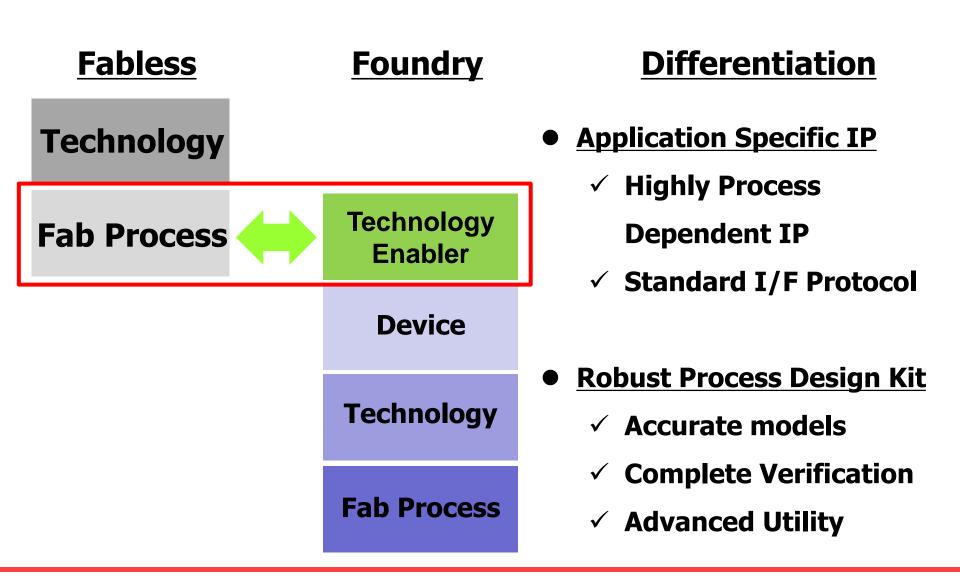
- Introduction
 - ✓ Specialty Foundry Strategy
 - ✓ Technology Enabling
- Application Specific IP (AS IP)
- Process Design Kit (PDK)
- Conclusion

Specialty Foundry Strategy





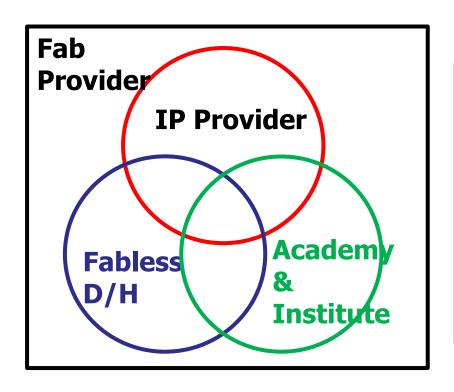
Technology Enabling



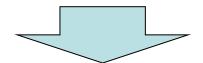
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AS IP: IP Clouding



AS IP Pool



IP Clouding

- Alliance with Customers
- Fab Driven
- Clouding & Raining

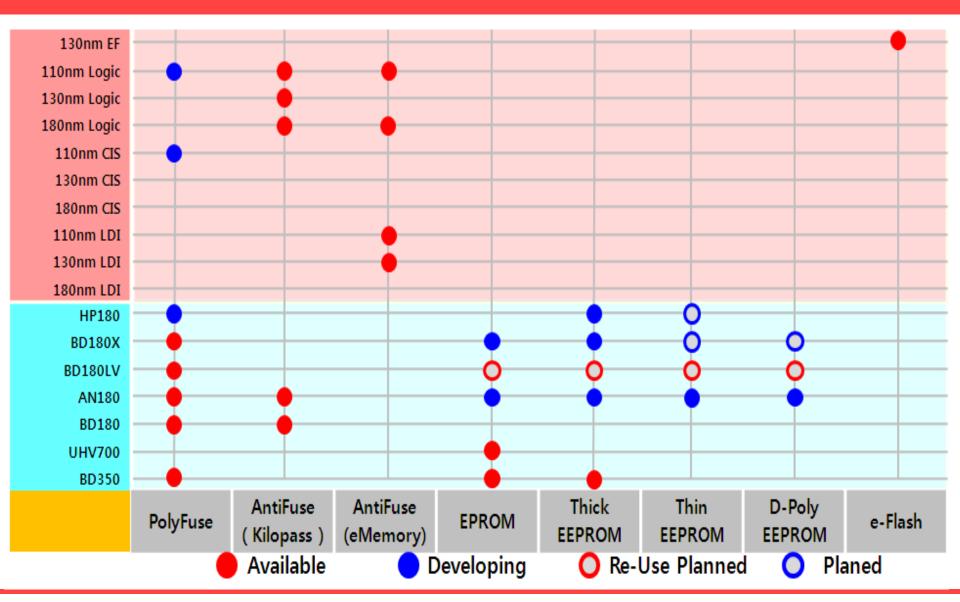
IP Clouding Program

- Alliance with Customers: Business with verified IP
- Fab Driven : Foundry dependent IP, shorten D/W
- Clouding & Raining: Verified IP Promotion

AS IP: AS IP in DBH

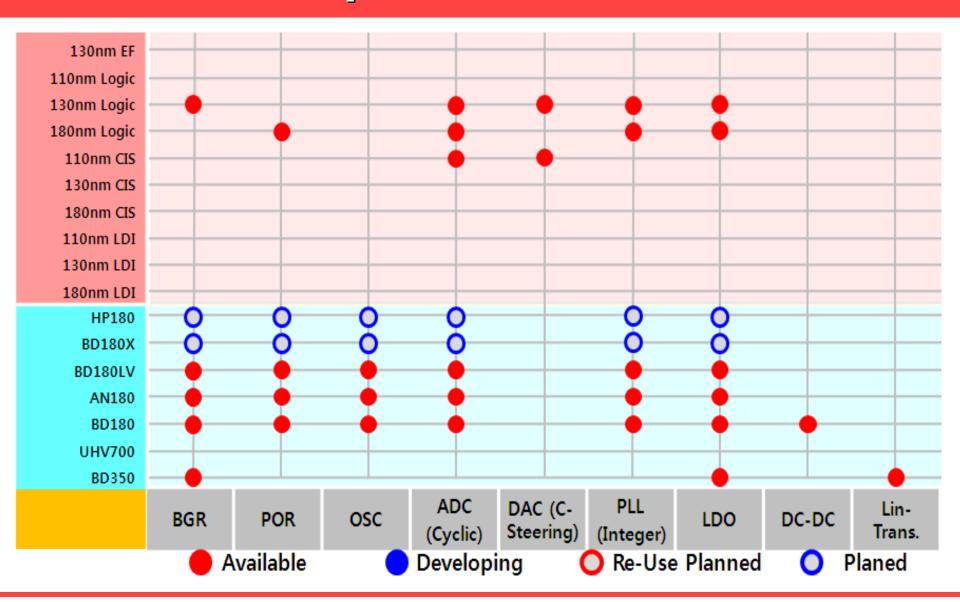
- ✓ DBH interested in followings
 - Memory Related
 : eFlash Memory, EEPROM, 32-bit MCU
 - Interface Related: MIPI (DSI, CSI), USB3.0
 - Basic IP: LDO, DAC, C-ADC, PLL, and so on

AS IP: NVM (OPT/MTP)





AS IP : Roadmap — Basic IP

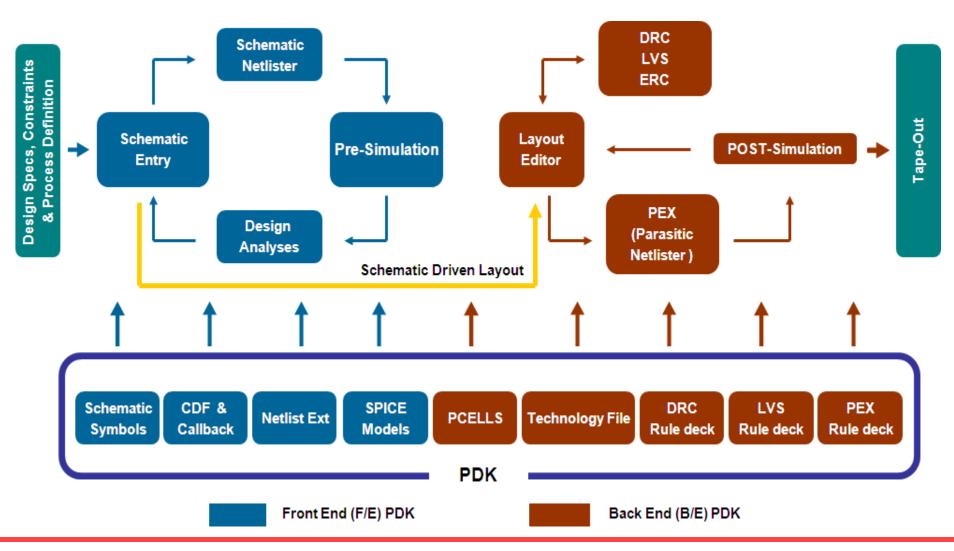




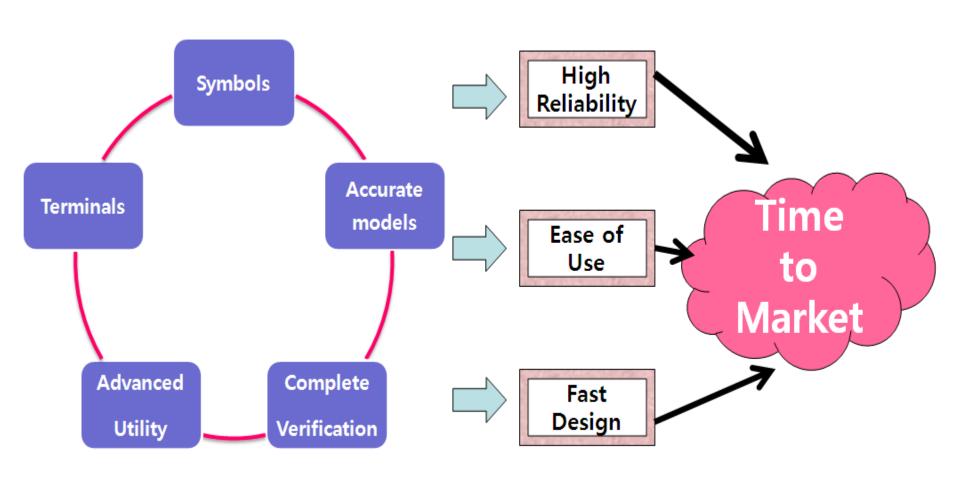
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PDK: What is the PDK?

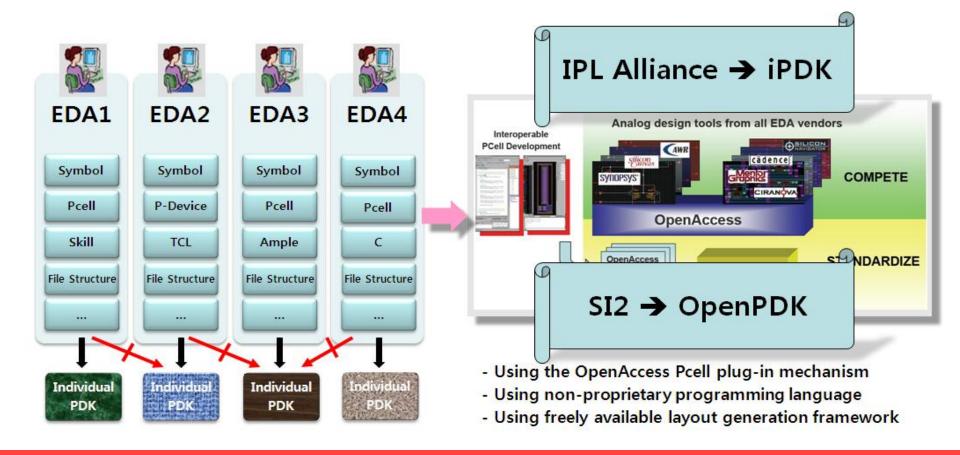


PDK: What Differentiates PDK?



PDK: PDK Activities

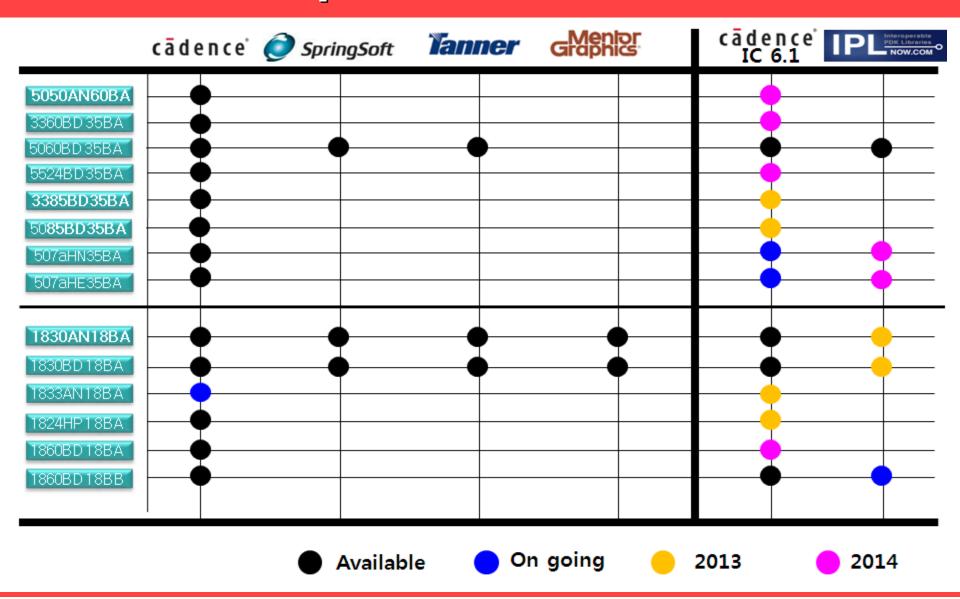
- Customers need PDK for their EDA tools
- **♦** Foundry has to support Multi-platform PDK(Cost, Consistency)
- **♦** Need Standard form of PDK



What is the Benefit of PDK Standardization?

- √ For End-user
 - Increased design chain choice
 - Access to best-in-class tools
- √ For Foundries
 - Faster service for customers
 - Lower costs to support EDA tools
- √ For EDA companies
 - No need for retooling to new formats
 - Can focus on value-added differentiation

PDK: Roadmap - BCD





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Conclusion

- ✓ AS IP and PDK are differentiation of Specialty Foundry
- ✓ AS IP interest in DBH
 : Memory IP, Interface IP, and Basic IP
 : IP Clouding
- ✓ Quality of PDK & Unified PDK structure
 : High Reliability, Easy to Use, Fast Design
 - : Multiplatform Support



Thank You